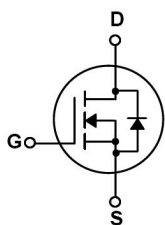
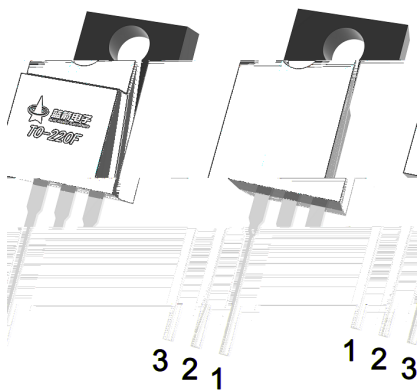


/ Descriptions

650V

/ Features**/ Applications****/ Equivalent Circuit****/ Pinning****/ Marking**

	θ		
	θ		

		μ				
						μ
		μ				
						Ω
						Ω
			Ω			

/ Electrical Characteristics(Ta=25)

		μ				

/ Electrical Characteristic Curve

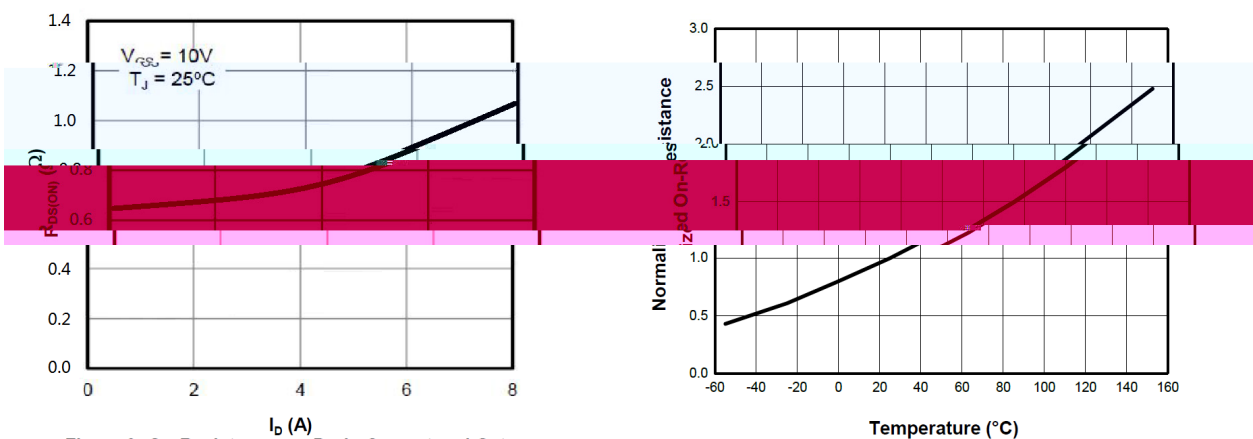
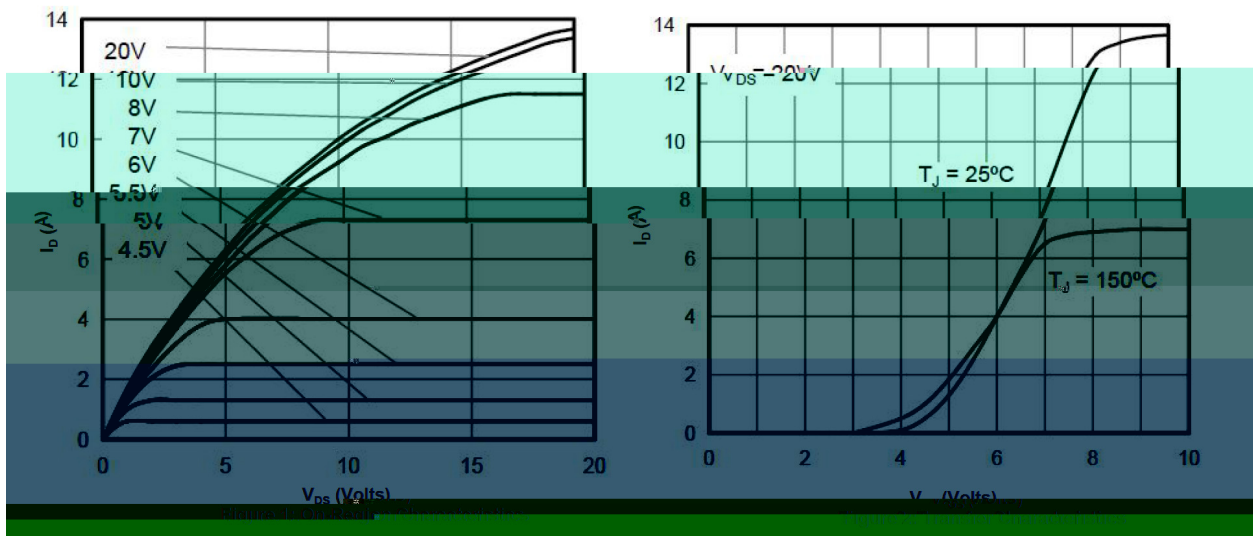
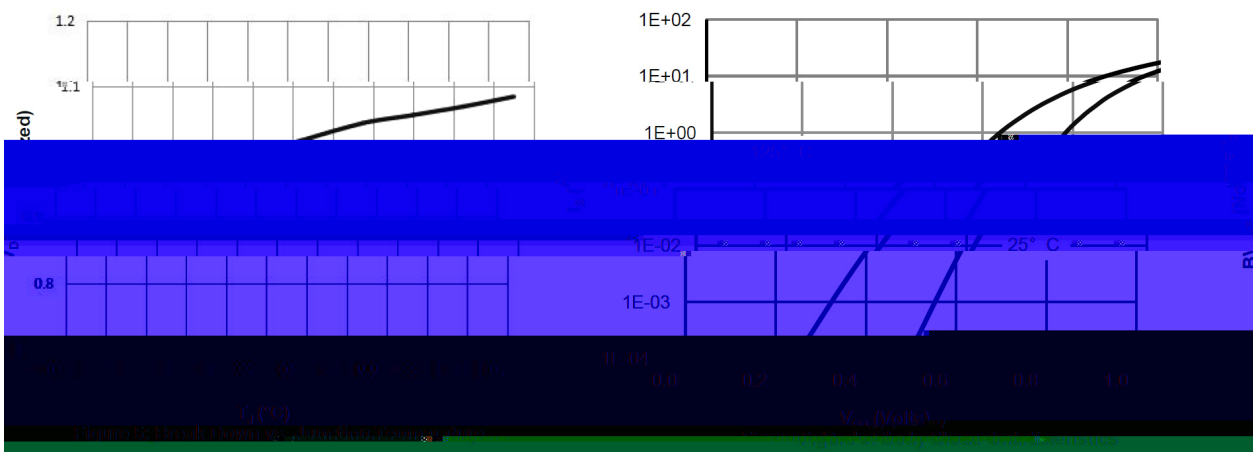


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

Figure 4: On-Resistance vs. Junction Temperature



/ Electrical Characteristic Curve

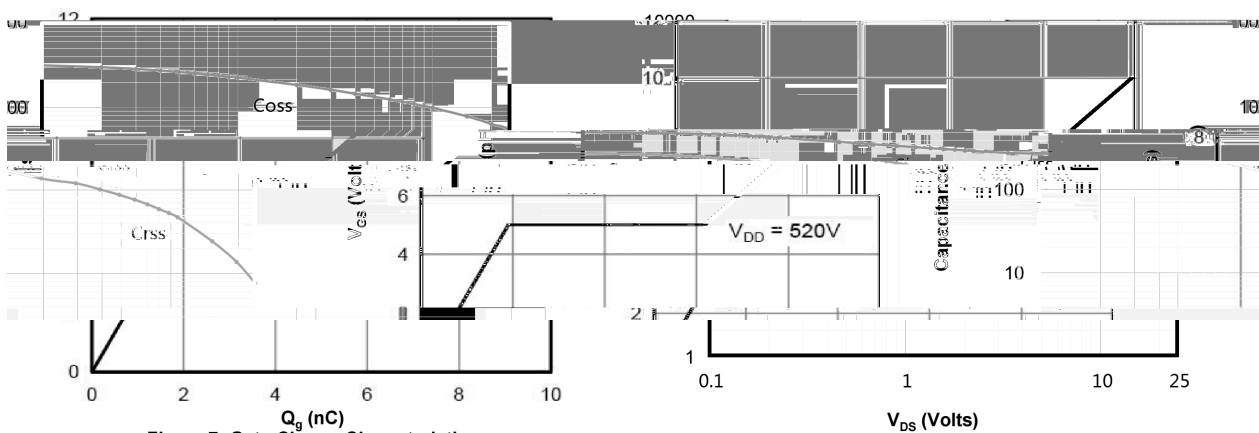


Figure 7: Gate-Charge Characteristics

Figure 8: Capacitance Characteristics

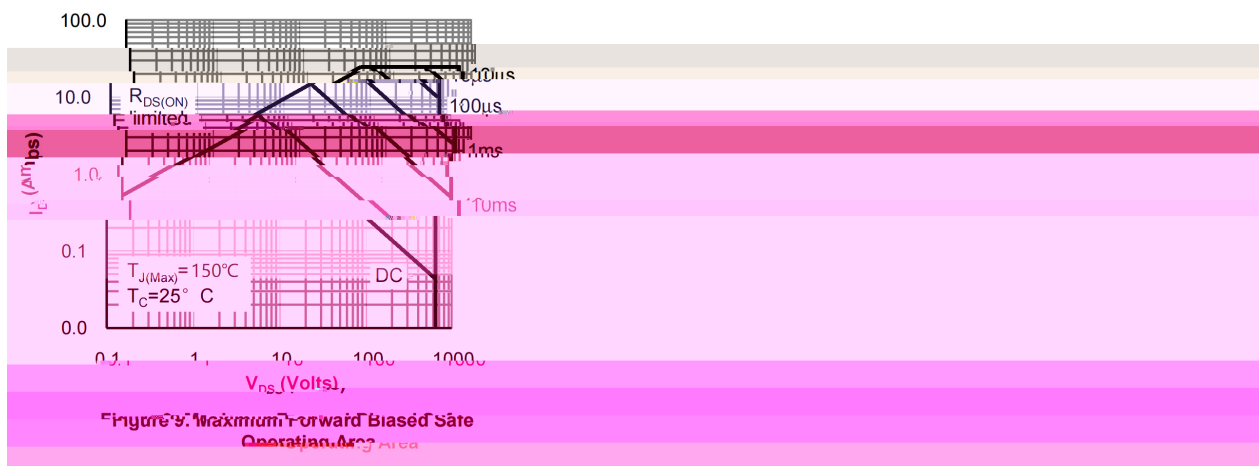


Figure 9: Maximum Forward Biased Safe Operation Area

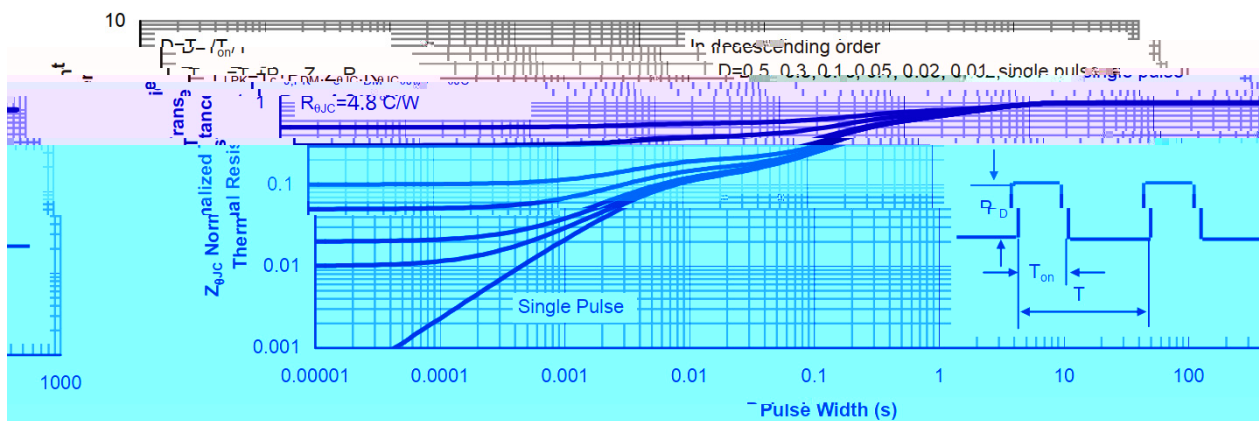
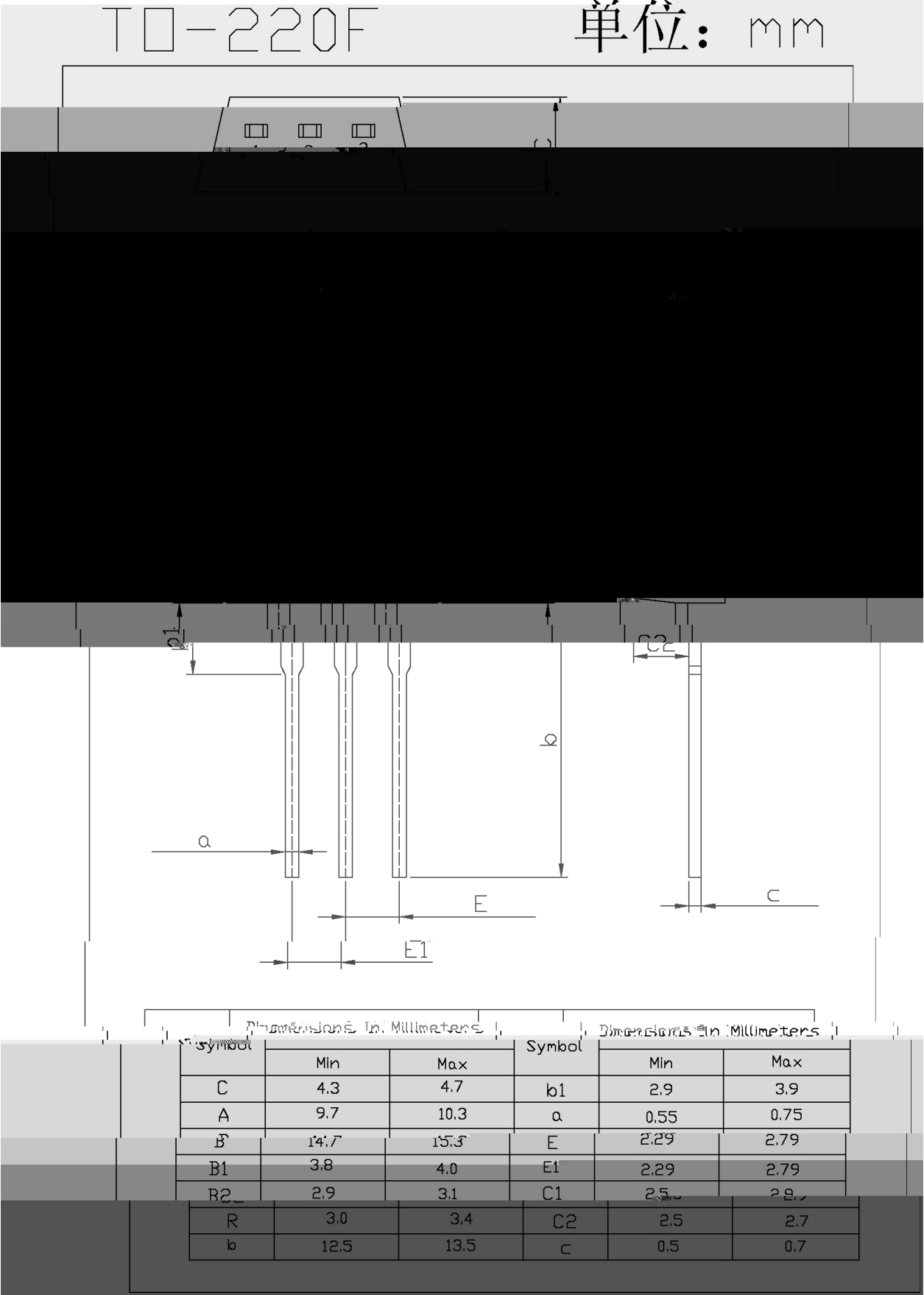
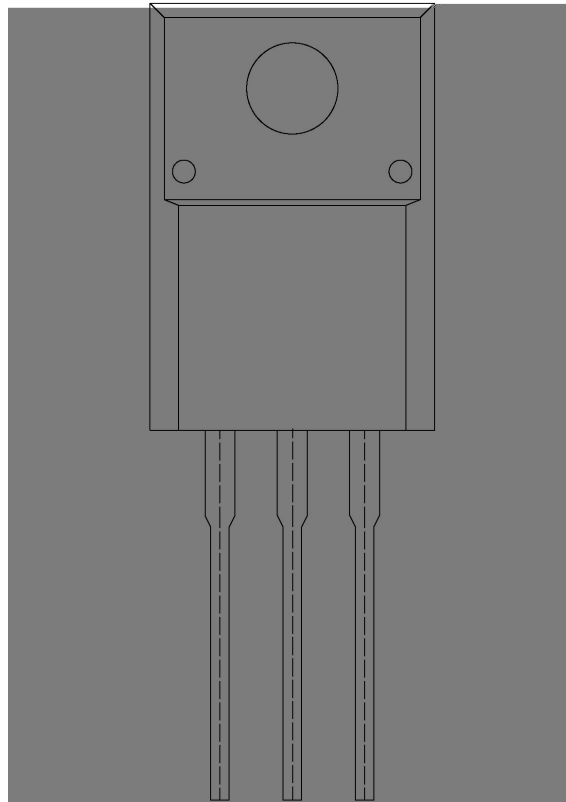


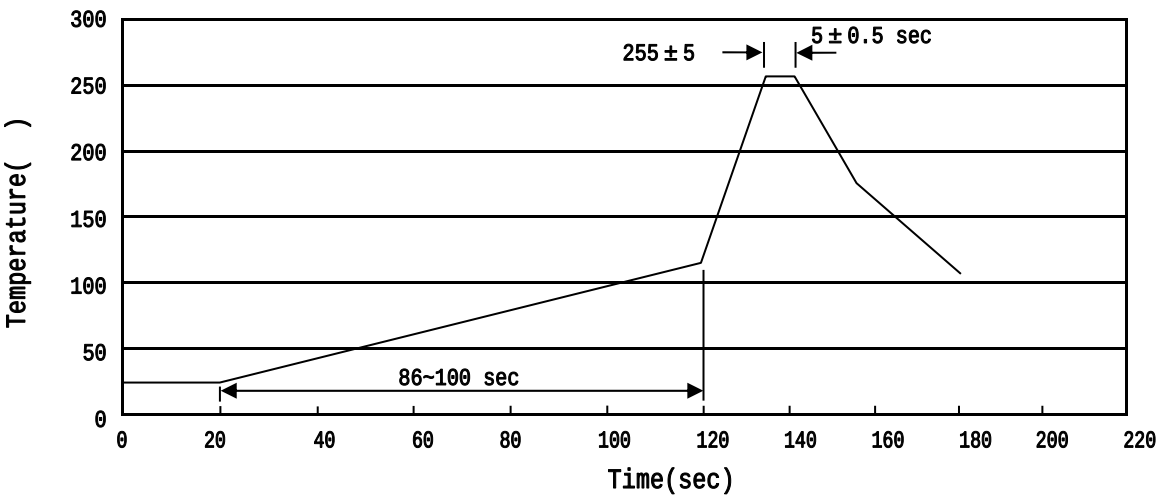
Figure 10: Normalized Maximum Transient Thermal Impedance

/ Package Dimensions





() / Temperature Profile for Dip Soldering(Pb-Free)



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.

/ Notices